

In the Claims

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1.-21. (Cancelled)

22. (Previously Amended) An electronic system comprising:

a ground having a ground voltage;

a transmission line having a first end with a first characteristic impedance, a second end with a second characteristic impedance, and a quiescent voltage at ground voltage;

a first termination device connected to the first end of the transmission line and connected to ground to provide an impedance substantially matched to the first characteristic impedance of the transmission line;

a second termination device connected to the second end of the transmission line and connected to ground; and

a first agent connected to the transmission line, the first agent comprising a die having a first core voltage, the die comprising a pMOSFET comprising a source at the first core voltage and a drain connected to the transmission line.

23. (Previously Added) The electronic system as set forth in claim 22, further comprising:

a second agent connected to the transmission line, the second agent comprising a die having a second core voltage, the die of the second agent comprising a pMOSFET comprising a source at the second core voltage and a drain connected to the transmission line.

24. (Previously Added) The electronic system as set forth in claim 23, wherein the first core voltage is not equal to the second core voltage.

25. (Previously Added) The electronic system as set forth in claim 24, wherein the first agent is connected at the first end of the transmission line, and wherein the first termination device is integrated on the die of the first agent.

26. (Previously Added) The electronic system as set forth in claim 25, wherein the second agent is connected to the transmission line at neither the first nor second end of the transmission line.

27. (Previously Added) The electronic system as set forth in claim 23, wherein the first agent is connected at the first end of the transmission line, and wherein the first termination device is integrated on the die of the first agent.

28. (Previously Added) The electronic system as set forth in claim 27, wherein the second agent is connected to the transmission line at neither the first nor second end of the transmission line.

29. (Previously Added) The electronic system as set forth in claim 22, wherein the first agent is connected at the first end of the transmission line, and wherein the first termination device is integrated on the die of the first agent.

30. (Previously Added) The electronic system as set forth in claim 22, wherein the first agent is connected to the transmission line at neither the first nor second end.

31. (Currently Amended) An electronic system comprising:

a ground having a ground voltage;

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a transmission line having a first termination device connected to ground to reduce signal reflections such that the transmission line has a quiescent voltage at ground voltage;

a die having a core voltage and comprising:

a pMOSFET driver to drive the transmission line, the pMOSFET driver having a source biased to the core voltage and a drain connected to the transmission line;
and

a second termination device coupled to the transmission line.

32. (Previously Amended) The electronic system as set forth in claim 31, wherein the second termination device comprises:

a nMOSFET driver coupled to the transmission line, the nMOSFET driver having a source at the ground voltage; and

a combinational logic circuit coupled to the nMOSFET driver; and

wherein the combinational logic circuit is coupled to the nMOSFET driver so that the nMOSFET driver has a first ON resistance when the pMOSFET driver is ON and a

second ON resistance when the pMOSFET driver is OFF, wherein the first and second ON resistances are not equal to each other.

33. (Previously Amended) The electronic system as set forth in claim 31, wherein the second termination device comprises:

a nMOSFET driver coupled to the transmission line, the nMOSFET driver having a source at the ground voltage; and

a combinational logic circuit coupled to the nMOSFET driver; and

wherein the transmission line has a characteristic impedance, wherein the pMOSFET driver and nMOSFET in combination have an impedance substantially matched to the characteristic impedance of the transmission line if both the pMOSFET driver and nMOSFET driver are switched ON, and wherein the nMOSFET has an impedance substantially matched to the characteristic impedance of the transmission line if the pMOSFET driver is switched OFF.

34.-39. (Cancelled)